



## PATENT ABSTRACTS OF JAPAN

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**H04H 5/00**(21) Application number: **04254639**(71) Applicant: **ROHM CO LTD**(22) Date of filing: **24.09.92**(72) Inventor: **ASHIDA HIROYUKI**(54) **FM/AM RECEIVING CIRCUIT**

## (57) Abstract:

**PURPOSE:** To reduce the number of the pins and parts of an FM/AM receiving circuit turned into an IC by sharing an AGC capacitor at the time of an AM reception and a synchronization detection signal smoothing capacitor at the time of an FM stereo reception.

**CONSTITUTION:** AGC circuits 30 and 31 smooth the signal of an AM detector circuit 6 by an AGC smoothing part CO, convert it into direct currents, and feedback-process it to an IF 5 as a bias voltage. And also, a synchronization detector circuit 18 receives an FM stereo, detects and smooths a pilot signal by a synchronization detecting and smoothing part, and prepares a signal for turning on an indicator 19. The AGC smoothing part and the synchronization detecting and smoothing part are shared, and constituted of one circuit CO. Then, the AGC circuits 30 and 31 are operated only at the time of the AM reception, and the synchronization detector circuit 18 is operated only at the time of an FM stereo reception, so that the overlap of a circuit operation can be prevented. And also, the AGC circuits 30 and 31 invert a detection signal, and decrease a voltage, and the circuit 18 detects the pilot signal, and increases the voltage, so that an erroneous

operation due to the overlap of the operation at the time of an AM/FM switching can be prevented.

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